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05 December 2017



# INTERNATIONAL SEMINAR ON POLICIES, INCENTIVES, TECHNOLOGY AND REGULATION OF SMART GRIDS

### CHANGING OUR VIEW OF THE WORLD

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"Introduction of smart grid concepts and technologies in all contexts generation, transmission, distribution, and commercialization will definitely change the business model of the present electrical sector." http://cigrebrazilrio2017.net/

#### WHAT IS YOUR VIEW OF THE WORLD?

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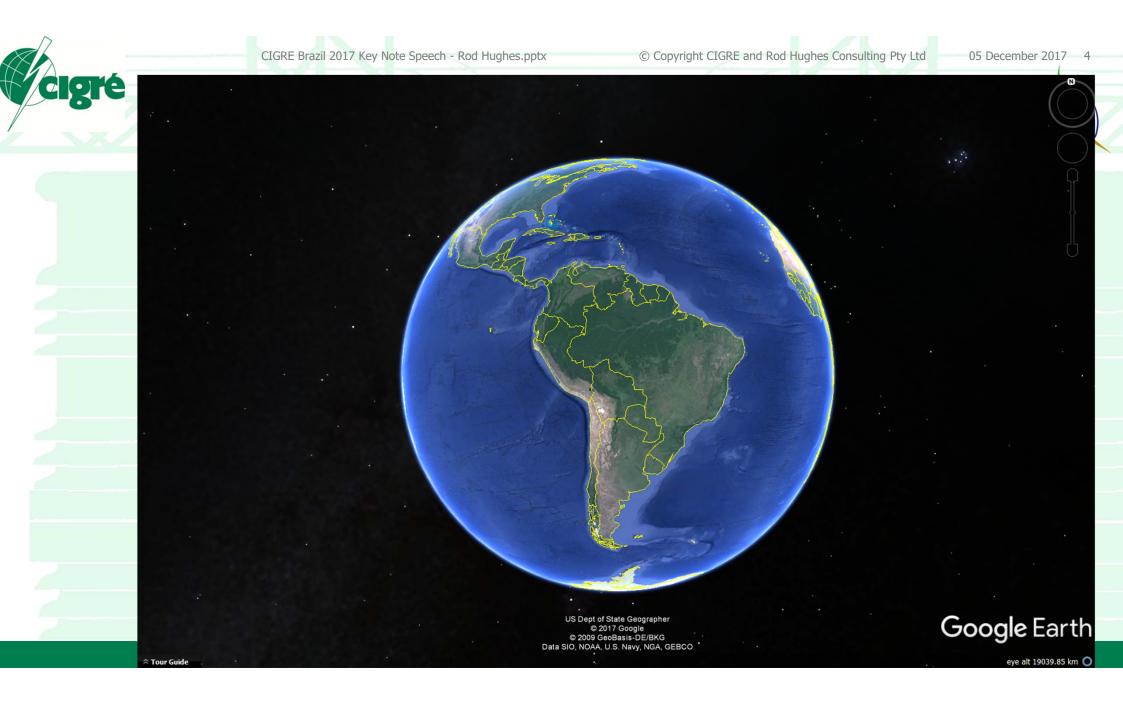
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US Dept of State Geographer © 2017 Google Image Landsat / Copernicus Data SIO, NOAA, U.S. Navy, NGA, GEBCO

Imagery Date: 12/14/2015 34°27'59.91" N 51°56'45.38" E eye alt 19039.85 km 🔘

Google Earth





☆ Tour Guide

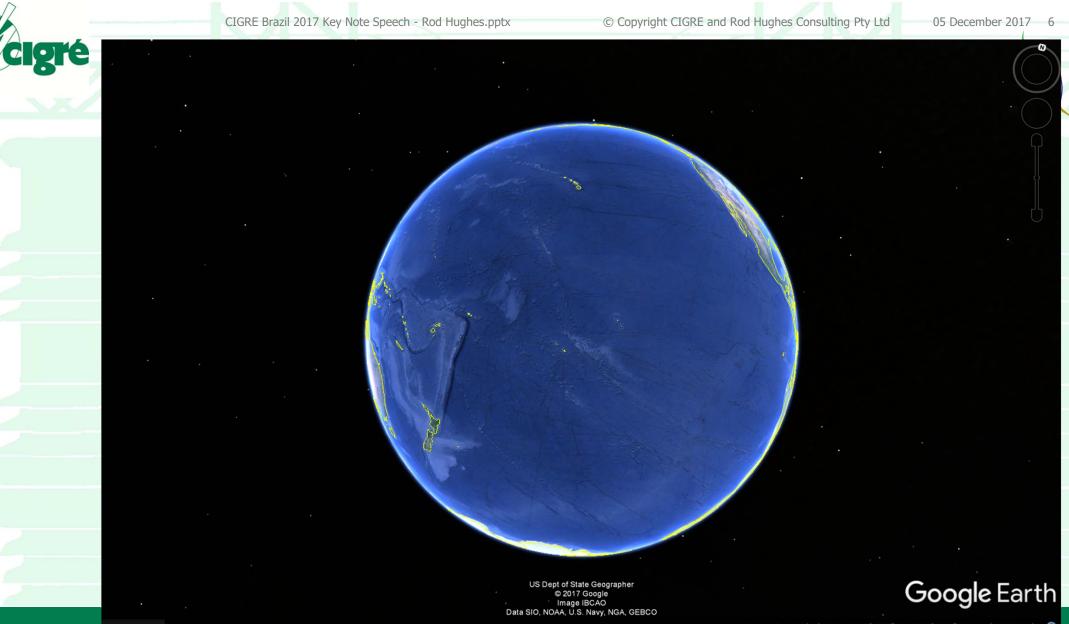
#### CIGRE Brazil 2017 Key Note Speech - Rod Hughes.pptx

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US Dept of State Geographer © 2017 Google Image Landsat / Copernicus Data SIO, NOAA, U.S. Navy, NGA, GEBCO Google Earth

Imagery Date: 12/14/2015 21°00'48.92" S 133°41'34.51" E eye alt 19039.85 km 🔘



☆ Tour Guide

Imagery Date: 12/14/2015. 13°51'43.78" S 146°33'15.63" W eye alt 19039.85 km 🔘



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## Your challenge here...

- What do I NEED to do differently?
- What CAN I do differently?
- What ELSE do I need to be <u>able</u> to do it differently?



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## Existing Risks We Live With

#### CT explosions and consequential damage/injury



UGRE BIAZII, RIO DE JANEILO, 4-7 DECEMBER ZUT7

## **Existing Risks We Live With**

- CT explosions and consequential damage/injury
- Not able to test full system
  - Performance assumptions work when needed, won't operate when it shouldn't
- Documentation errors
  - testing mistakes, blackouts, loss of life
- Wiring errors

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- possible loss of life, blackouts, consequential damage due to uncleared faults
  - Extensive wiring testing by humans to identify human errors
- Multiple communication systems
  - Interfaces to interfaces, \$\$\$\$, Performance constrained by oldest/least Is this how you would want technology to build a grid from nothing?
  - ..... and many more



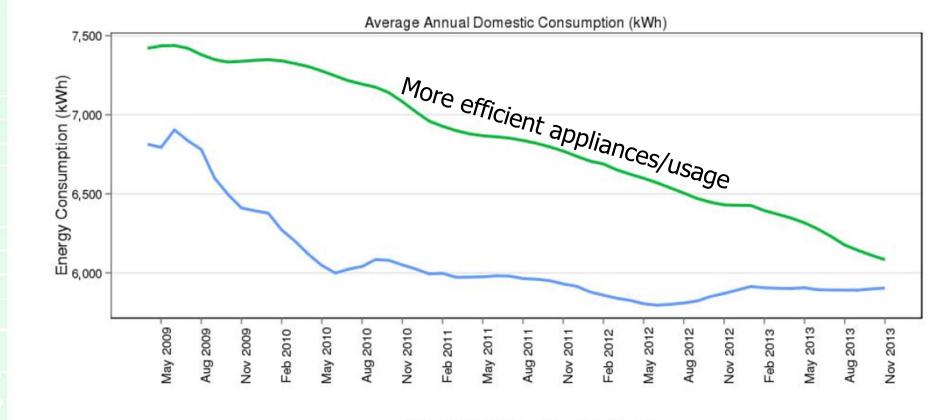


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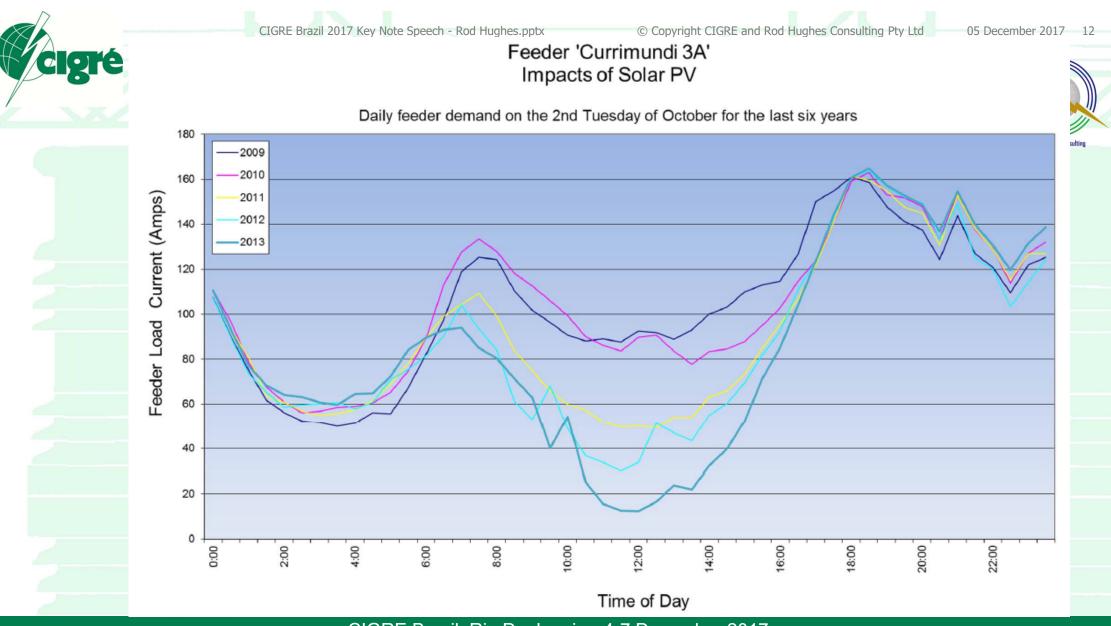
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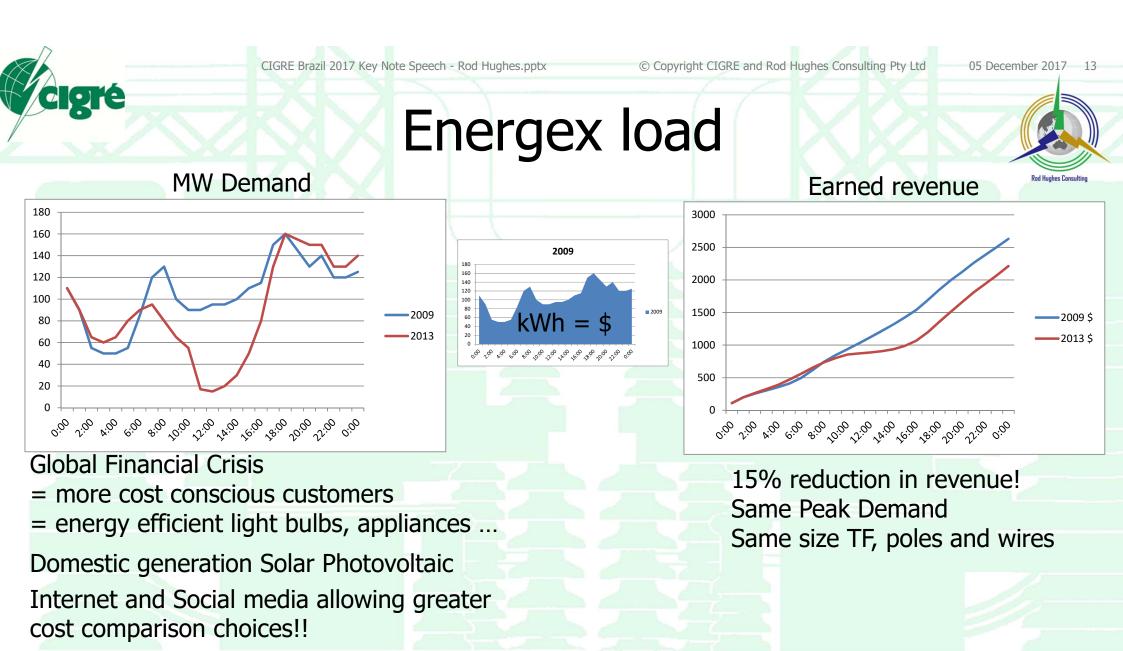
**Rod Hughes Consultin** 

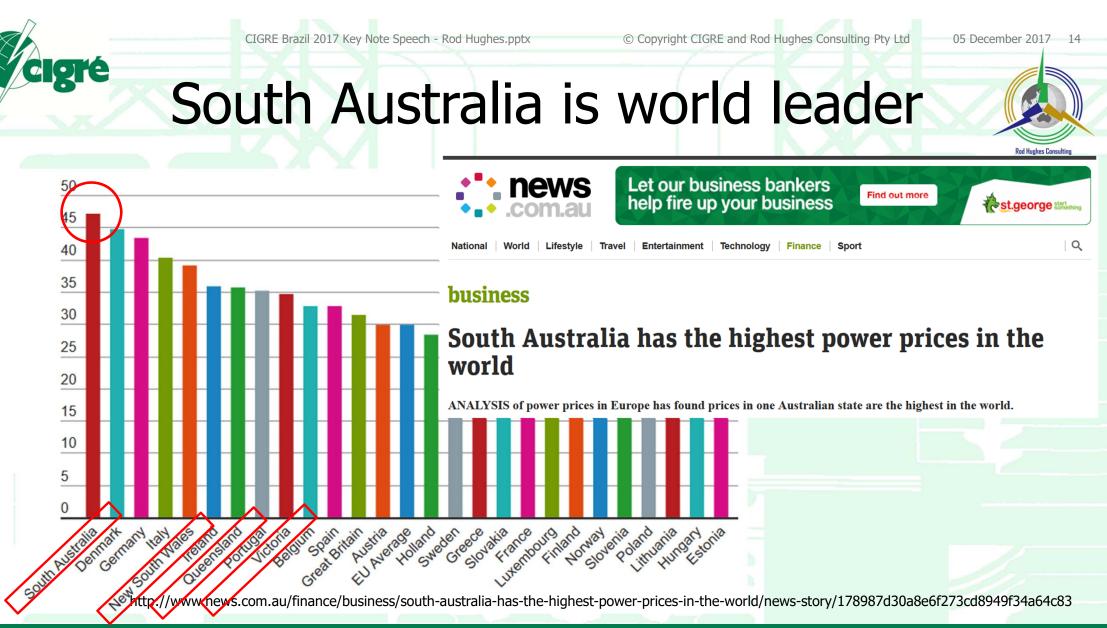
## **Reduction in consumption**



- Domestic (no solar) - Domestic (with solar)







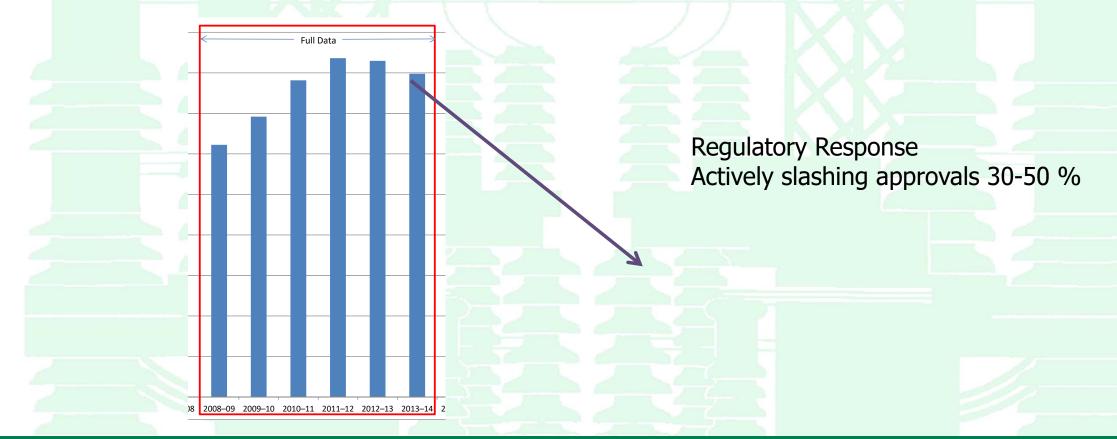


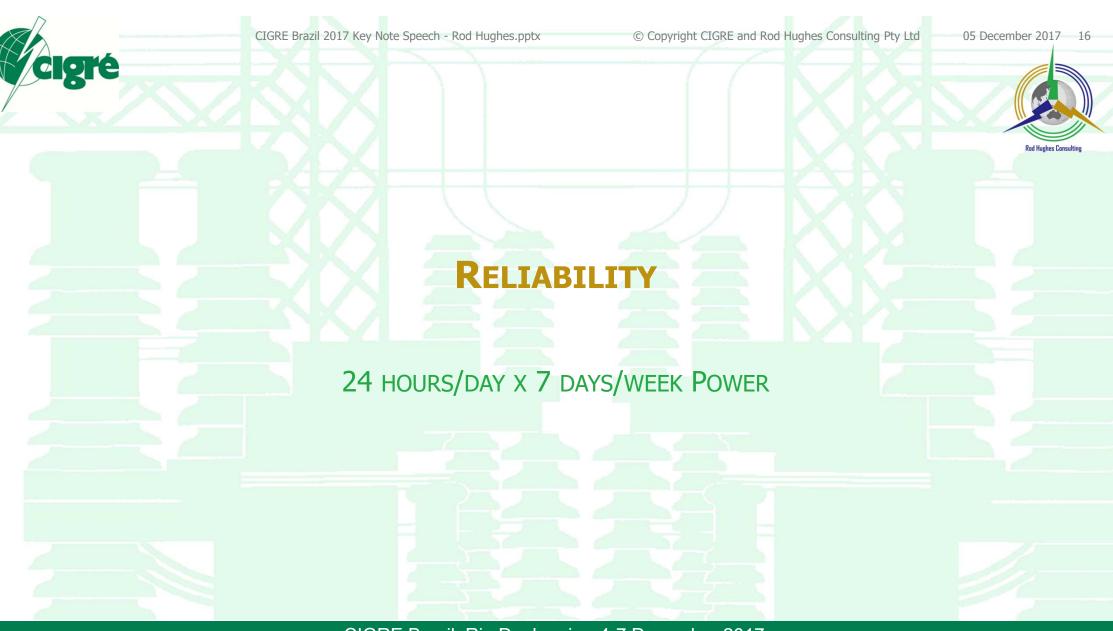
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## Gold plated networks!!!





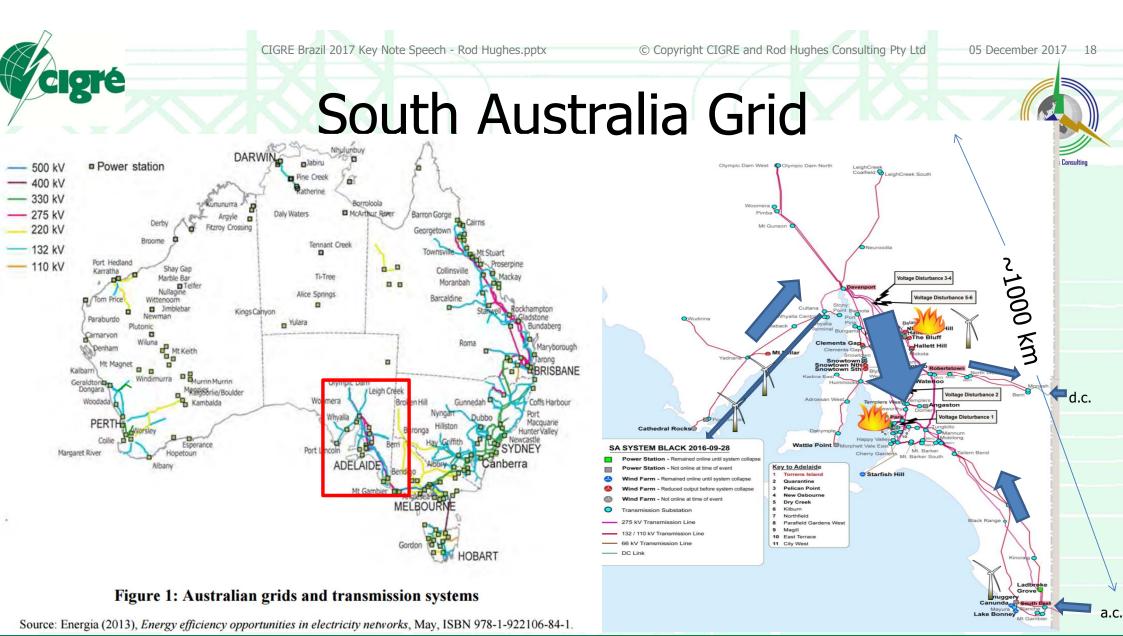


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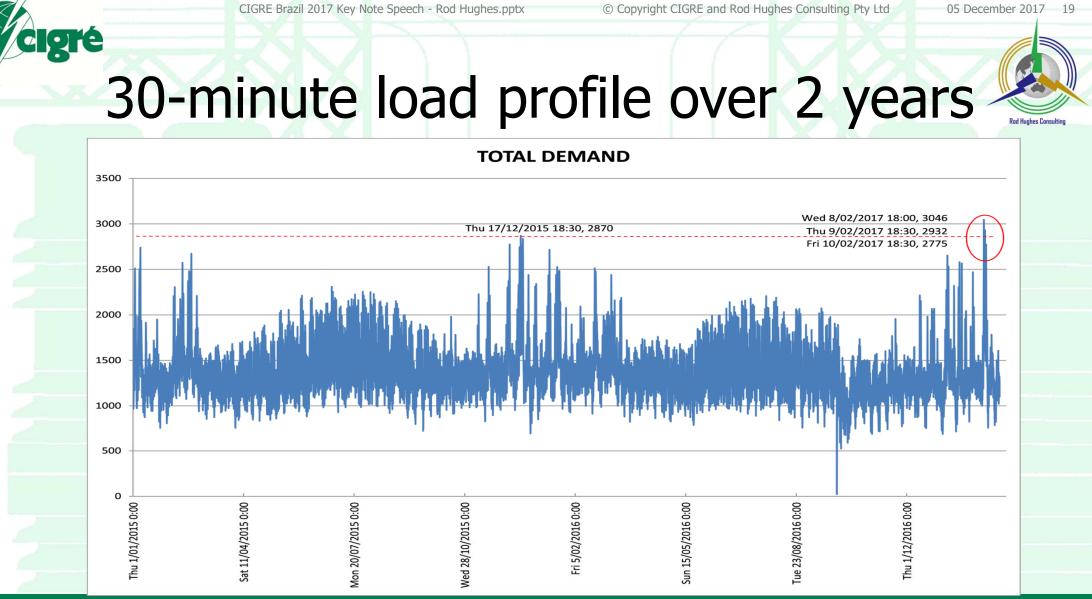


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### What Did South Australians Use Before Candles?



CIGRE Brazil, <u>Rio De Janeiro, 4-7 December 2017</u>



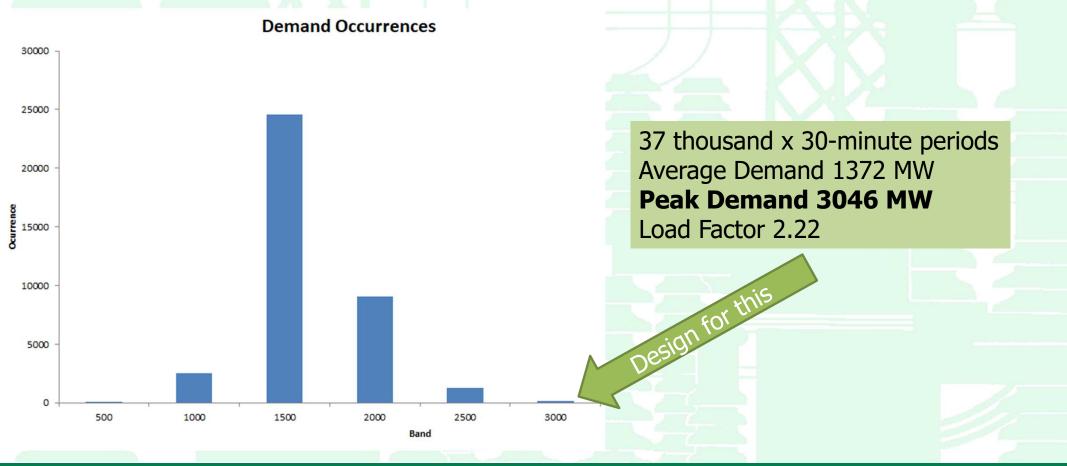


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### **Design for Peak Demand**



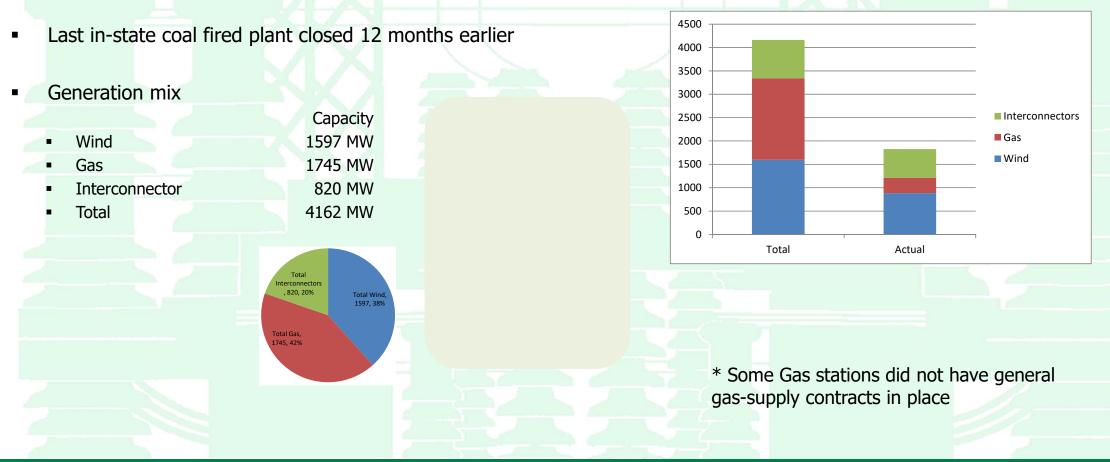


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## Wednesday 28 Sept 2016



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# Wed 28 Sept 2016 4:18 pm

- STATE WIDE BLACKOUT
  - Interconnector segregation
  - Black Start !!!!

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#### **IT'S OFFICIAL:**



Adelaide reaches zero emissions

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# Wed 28 Sept 2016 4:18 pm



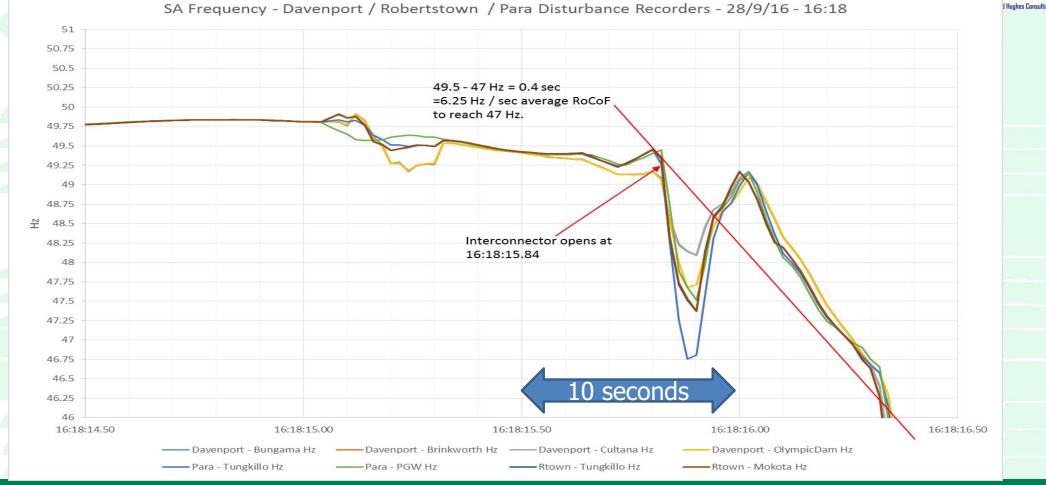
- Wind THE culprit! (... but not the turbines)
  - "we suffered at least seven near simultaneous TORNADOS gusting over 220 km/h in places!!"
  - Three 275 kV Tx lines cut
  - 27 towers fallen

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... but we can't blame God!!

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## Load Shedding?



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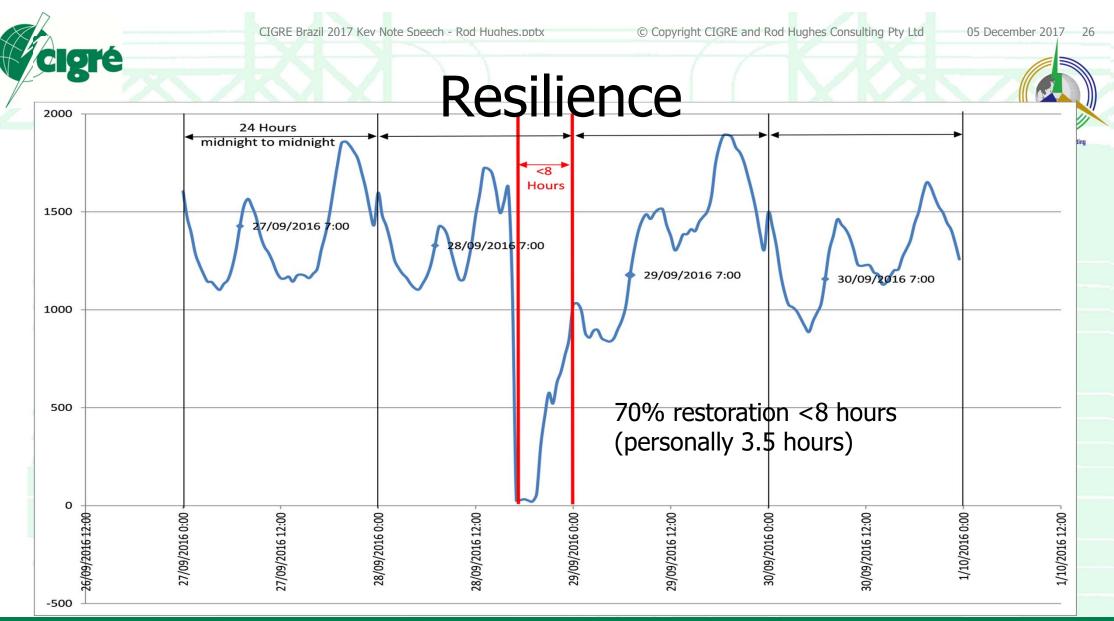
# Wed 28 Sept 2016 4:18 pm



- Wind Turbines the "culprit"
  - Performed better than "expected" rode through 6-8 seconds of faults
  - "Unknown" wind turbine control setting to trip if 5 or 6 grid faults detected in short time
  - A.C. interconnector overloaded
  - State Black!

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- Australian Energy Market Operator Report: <a href="https://aemo.com.au/Media-Centre/Review-of-the-Black-System-South-Australia-Report-System-event-of-28-September-2016">https://aemo.com.au/Media-Centre/Review-of-the-Black-System-South-Australia-Report-System-event-of-28-September-2016</a>
- My commentary <u>https://www.linkedin.com/pulse/sa-blackouts-generation-mix-rodney-hughes</u>



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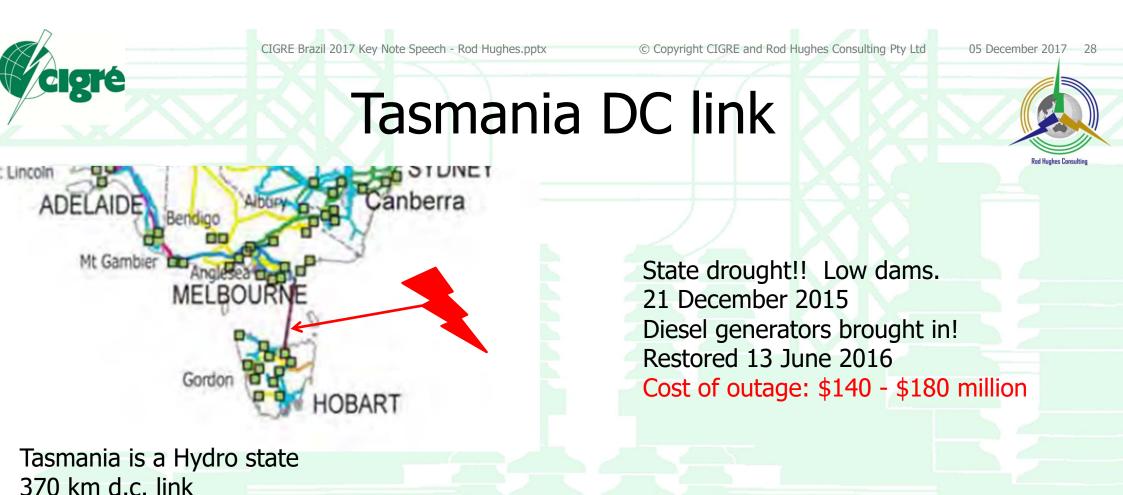
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## **General Strategies**

Change turbine setting to "8"!

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- Minimum level of synchronous generation online.
  - changes with increase in non-synchronous generation source: system studies
- Reviewing performance of distance protection under low fault levels assessment of adequacy.
- Improving system strength in the short term
  - by taking actions such as load shedding at specific predetermined conditions interconnector flows, combination of synchronous generation.
- Assess the use of wind turbines as synchronous condensers to increase system strength,
  - looking at capacity, location/s, performance requirements.



Bring cheap power to the mainland

Average net benefit >\$40 million per annum

Commissioned 2006



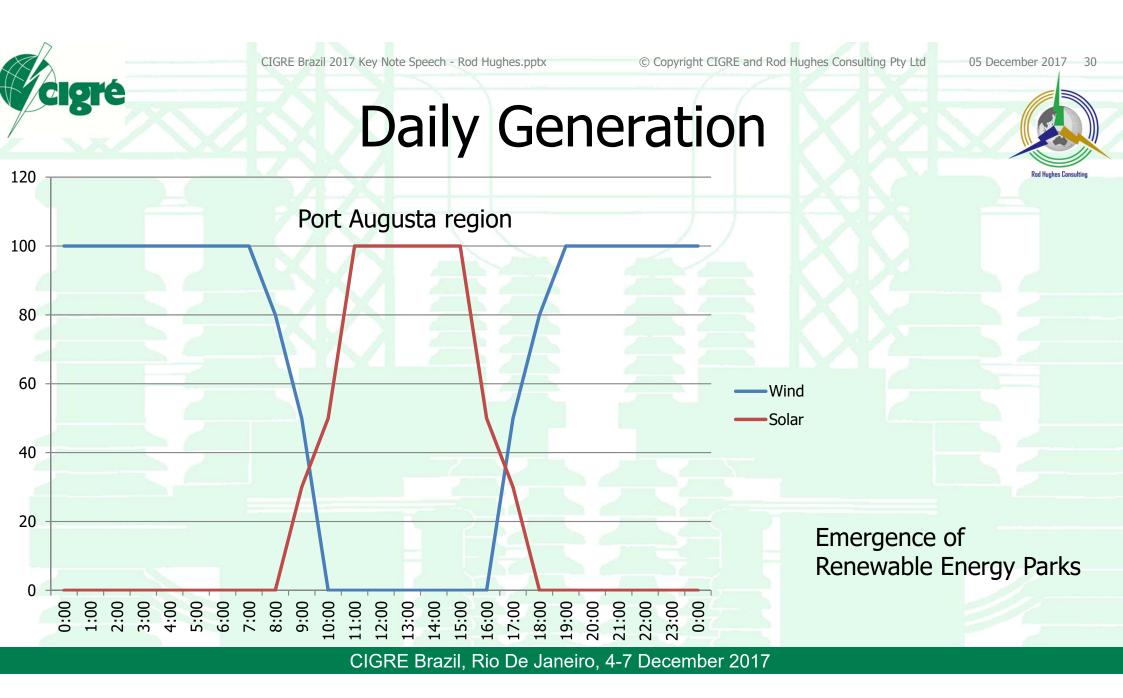
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## Should we have known?



- "Good infrastructure will not always be provided in a timely manner and adequate scale by the market."
- "Adaptation to climate change and more frequent disruptions of electricity supply will require deeper interconnection capacity."
- "Climate impacts and pressures on electricity infrastructure are forecast to increase and include changes to demand for electricity, more rapid deterioration of assets, and increased network failures resulting from severe weather events"
- => Government funding of new transmission assets
  - Deregulated market!!

# new interconnectors since 2010? ZERO





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## Batteries

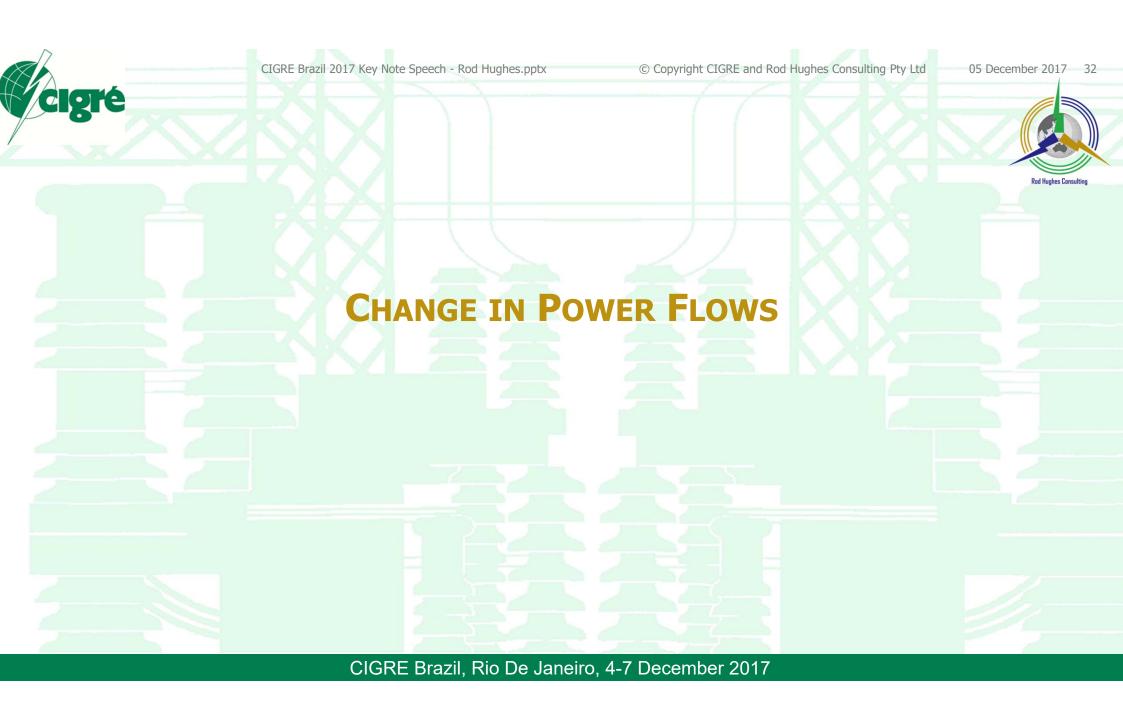
Elon Musk makes a bet to fix a state's energy woes in 100 days, or it's free

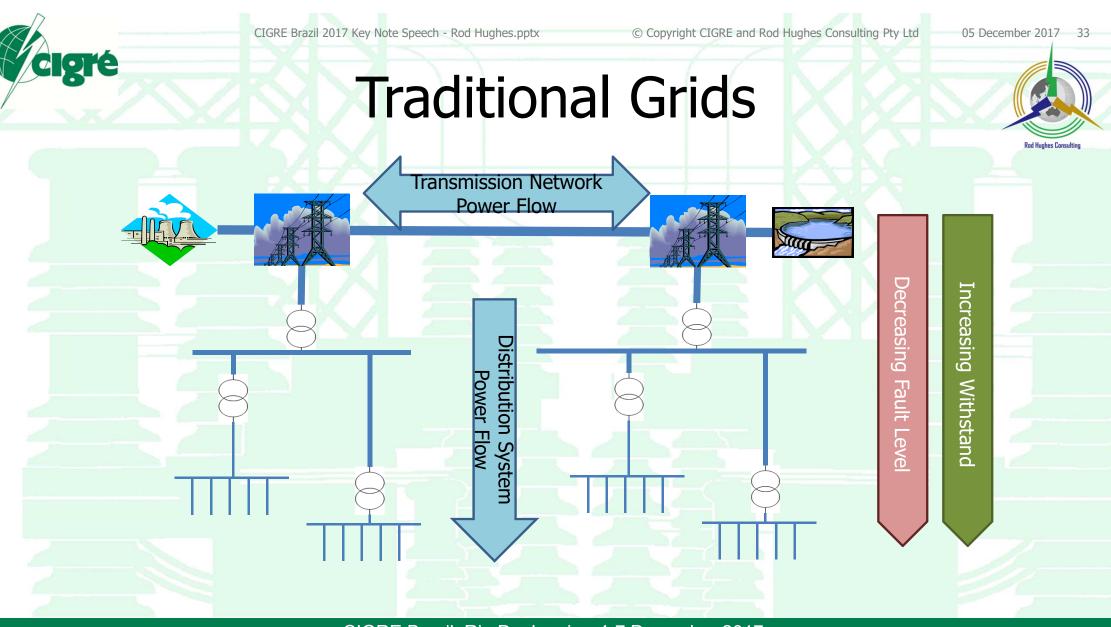


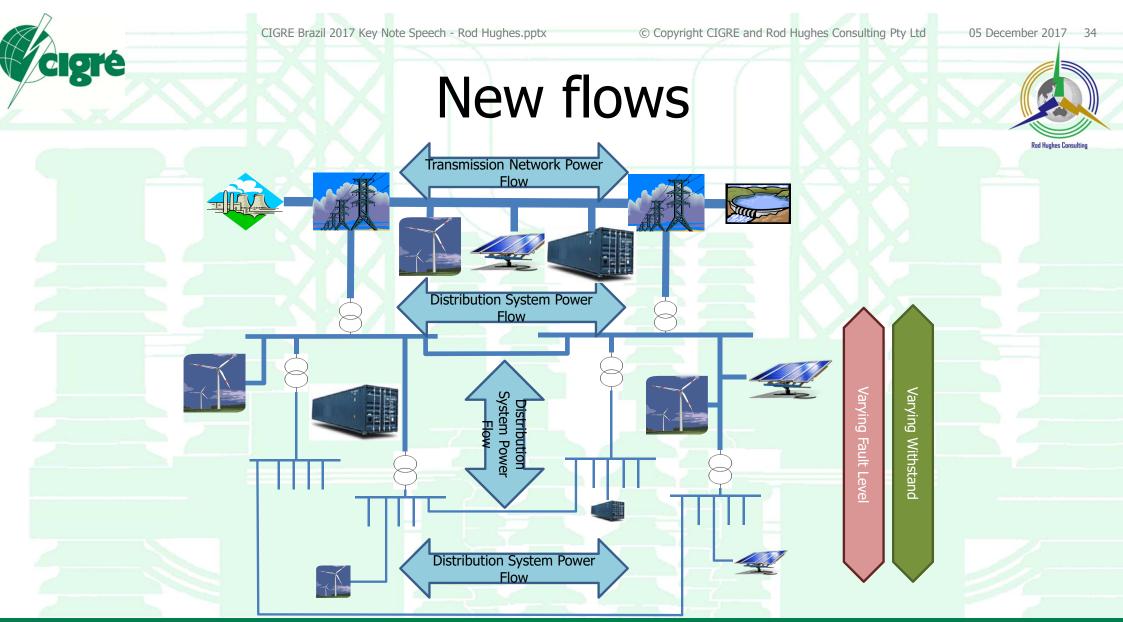
The world's largest lithium-ion battery 100 MW 129 MWh

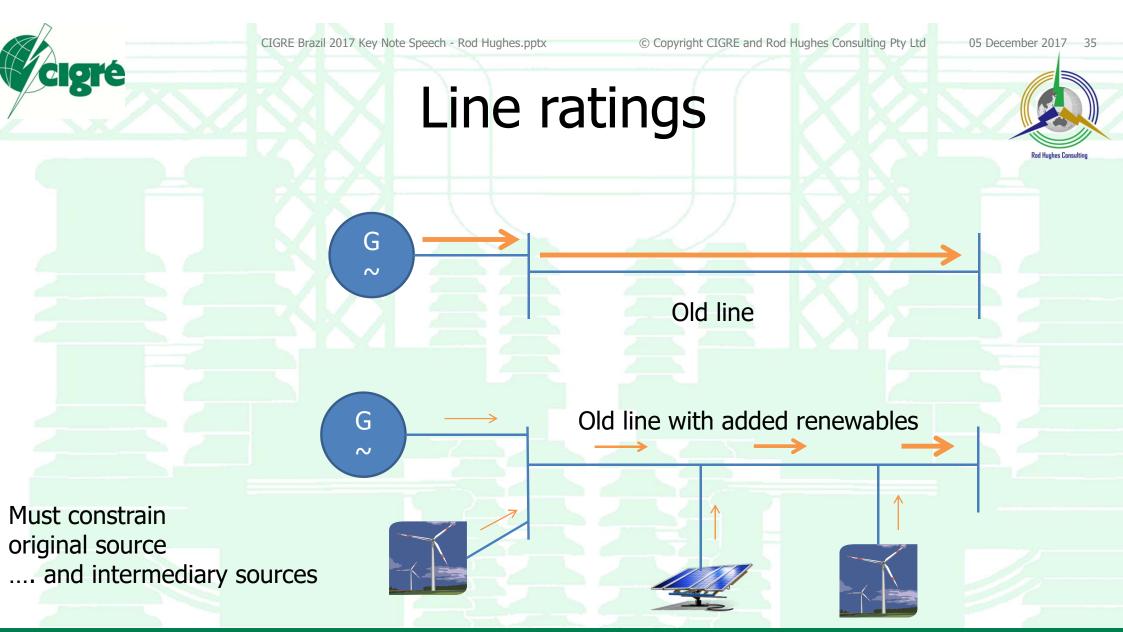
Project planning and contracting actually started ~ 12 months earlier

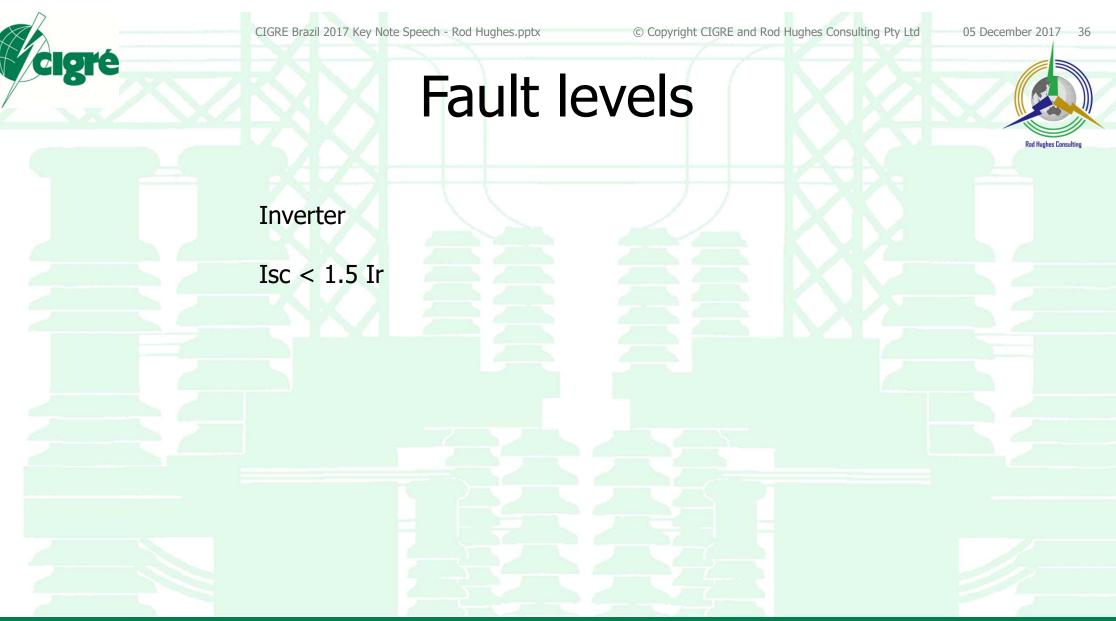
MIGE REVORKOUNDEZIN/GETTY MIGES















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#### "SMART GRID" "BIG DATA" "IOT"

#### TRANSMITTING INFORMATION ABOUT THE POWER SYSTEM FROM "A" TO "B"



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# Old systems

Relay wires and SCADA DNP3 ... "happy as a pig in mud"

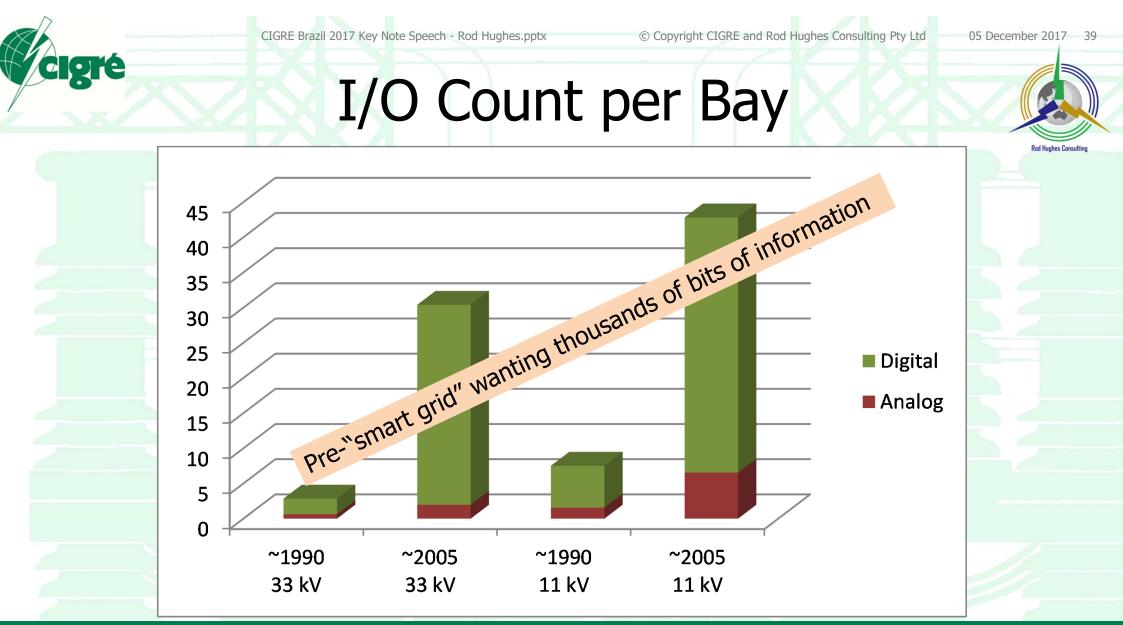
More data, different names by different vendors

Not a sustainable proposition

We need common semantics!!

...but the mud pit is at the front door of the abattoir

LandLearn NSW



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## **Ultimate Objective**

Power system engineering is carried out by

multiple engineers

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- in multiple departments
- in multiple organisations
- over multiple phases
- in multiple primary and secondary projects
- coherently deployed over the next "100 years" asset usage
- incorporating hundreds of functions,
- dozens of different IED box types
- from dozens of different vendors
- to enhance reliability, reduce risks, provide Reusable Engineering and reduce costs.



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#### IEC 61850 is not inherently cyber secure CIGRE Electra Magazine ELT\_229\_3 Dec 2006

#### **7** Conclusions and Future Developments

The level of security of the older and most current SCADA systems is not enough for the present cyber situation.

#### To make things worse the new IEC-61850 standard has no provisions for security yet.

Because of its open network type communication, it also opens the system for cyber attacks.

This **new communication standard is really dangerous** if used in a network with a poor security design.

Because the protocol does no longer compartment the communication, **it may loose control over the whole grid**.

All older communication standards do not address security [either].



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# IEC 61850-3 Ann A

- The SAS should implement security features that counter, within appropriate user and cost constraints, the following threats:
  - Denial of service this threat attempts to deliberately impede legitimate access.
    - Therefore, appropriate counters should be determined on a system by system basis and are not subject to standardization within the scope of this standard.

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### The Maroochydore Incident





Vitek Boden March and April **2000** 46 attempts to take control of the sewage system - caused millions of litres of raw sewage to spill out into local parks, rivers and even the grounds of a Hyatt Regency hotel. Marine life died, the creek water turned black and the stench was unbearable for residents

#### STUXNET 2010

Iranian Nuclear Power Plant cyber attack

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## NERC CIP-004-6 Table R5

Reliability Standards for the Bulk Electric Systems of North America

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Part
5.1

- 1. Know who has access to what.
- 2. Revocation is critical!



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#### Access for who? For what?

- Engineering personnel
- Commissioning personnel
- Maintenance personnel
- Vendor support personnel

- Geographical region
- Site specific
- Device Specific
- Command Specific
- Role Specific View Edit

CIG	"it is not a	© Copyright CIGRE and Rod Hug ecurity problem" problem"	hes Consulting Pty Ltd 05 December 2017 47 Queensland Land: 1.8 million km <sup>2</sup> Population: 5 million
	SCADA RTUs	Voltage Regulators (MV)	Brazil Land: 8.5 million km <sup>2</sup>
	Terminal Server	Distribution Transformer	Population 206 million!
	Protection Relays	rik n Regulators	
	Reclosers	sformers	
	Fault Indicators (LFI,RMU, Fuse savers)	EDS1 adons	
	Statistical meters	• The Sensors (Overhead	d)
	Smart Revenue meters EXPlo3850	5 Underground Cable Mon	itoring
	Reclosers Fault Indicators (LFI,RMU, Fuse savers) Statistical meters Smart Revenue meters Programmable Logic Controllers (PLC by 2 Substation Pattory Charger	Embedded Generation (u	utility owned) GUSS
	Substation Battery Charger	Embedded Generation (u	utility owned) RUSS
	Backup Generator Controller	Embedded Generation (3	3rd party)
	Main Generator Controller	Electric Vehicle (EV) cha	rging stations
	Voltage Regulators (HV)	Inverter Energy Systems	;



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## Organisation, not Technology



- "Technology is not the barrier to adoption. The fundamental issue is organization and prioritization to focus on those first aspects that provide the greatest customer benefit toward the goal of achieving an interoperable and secure Smart Grid."
  - IEC

http://www.iec.ch/smartgrid/downloads/sg3\_roadmap.pdf



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#### **IEC 61850 IS NOT A MERE PROTOCOL**

#### IT IS A VENDOR-INDEPENDENT ENGINEERING PROCESS TO CONFIGURE IEDS TO COMMUNICATE



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# ENSTO-E

European Network of Transmission System Operators for Electricity

- 41 Transmission utilities
- Europe first deployments of IEC 61850
- Largely vendor specific projects initially
  - Laufenburg ....
  - 8 years experience



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#### ENSTO-E Letter April 2012 https://tinyurl.com/y7alhyrk

- IEC 61850 does NOT work!!
  - Instantaneous interoperability between suppliers
  - Engineering efforts hard
    - Need mature standardized third-party tool
      - market to clearly move to a top-down approach (i.e. not just vendor tools)
    - TSOs are unable to cope
      - technical knowledge and skills that are required
  - Interoperability over the lifetime



## **Recipe for success**

- Most of the issues are due to
  - incomplete specifications of the users,
  - incomplete implementations in products and
  - Iack of knowledge of the key issues of the standard with
  - limited (no) investment in tools



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#### Beware of the "Narbonne Syndrome" \*

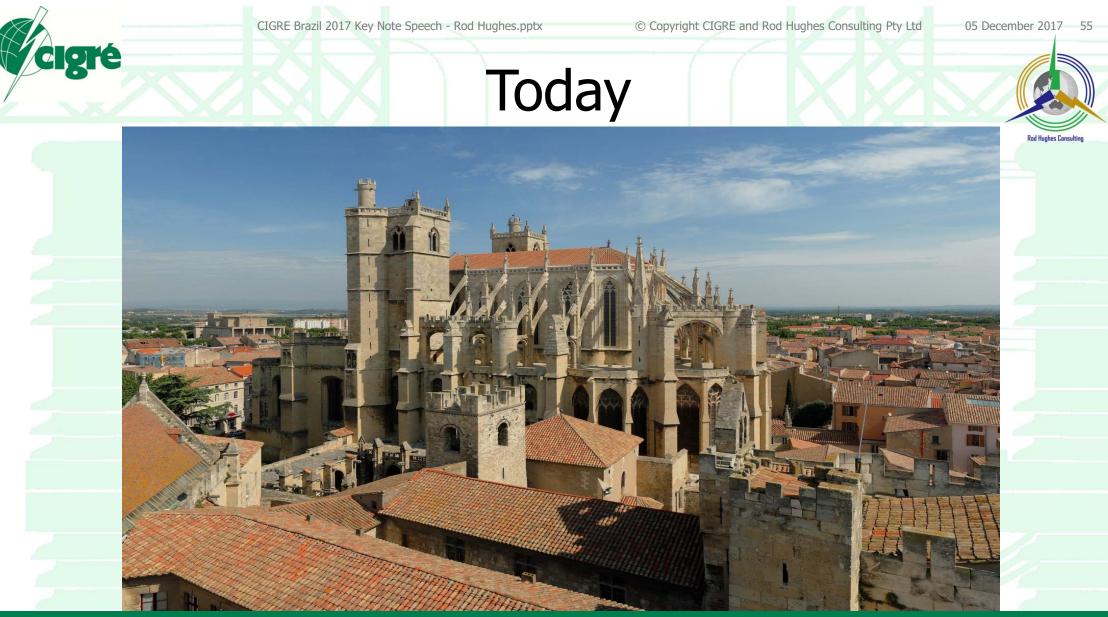
- 313AD first Basilica
- 441AD fire
   37 years to demolish remains
- 445AD new Basilica
- 890AD Cathedral
- 1272AD start new Gothic Cathedral
  - to be bigger than Notre Damme in Paris

... And now it looks like:

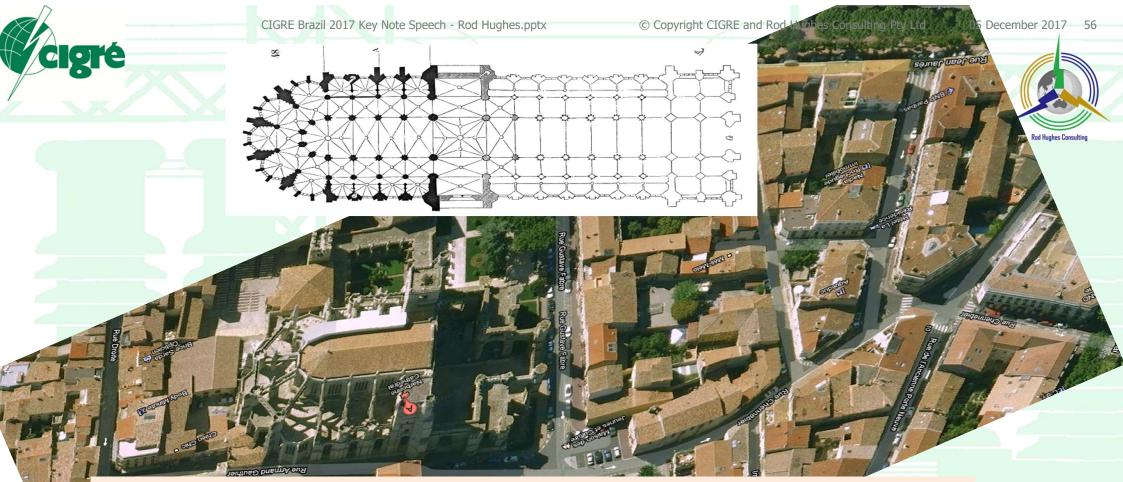
CIGRE Brazil, Rio De Janeiro, 4-7 December 2017



\* not an official syndrome Term created by Rod Hughes relative to IEC 61850 implementation planning



http://en.wikipedia.org/wiki/Narbonne\_Cathedral



- River silted up port trading economy of city died, no more funding
- Location would have needed destruction of precious fortification walls!
  - medieval times in France hated the Church hence Rome and Avignon (as home of 9 Popes) are fortified walled cities to protect the Church
- But it is used every week even now ... just less than intended 😕



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## Working the Plan



 when you're up to your neck in alligators, it's easy to forget that the initial objective was to drain the swamp!

- Start by stop feeding the alligators it only encourages them
  - + Make a strategic <u>significant</u> decision to change <u>significantly</u>



- Introduction of IEC 61850 and its impact on protection and automation within substations CIGRE Technical Brochure 326 Fig 20
  - Leverage experience
  - Seek power engineering context
  - Integrate specialist domain expertise: Protection, SCADA, Primary ... IT CIGRE Brazil, Rio De Janeiro, 4-7 December 2017

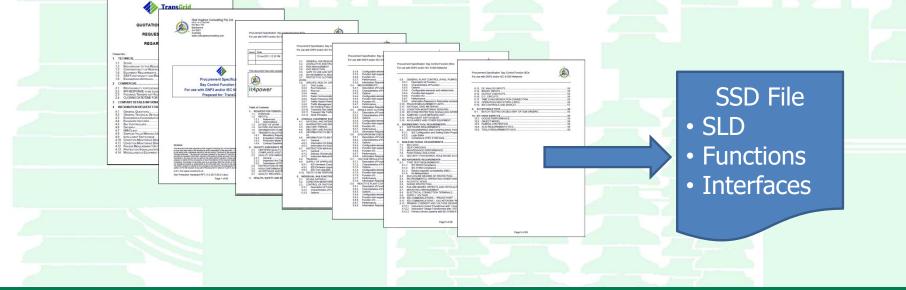
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# **Design Concept Specification**

- You have no right to expect to receive the things you want if you do not specify them!
  - Develop the specification in re-usable SCL files





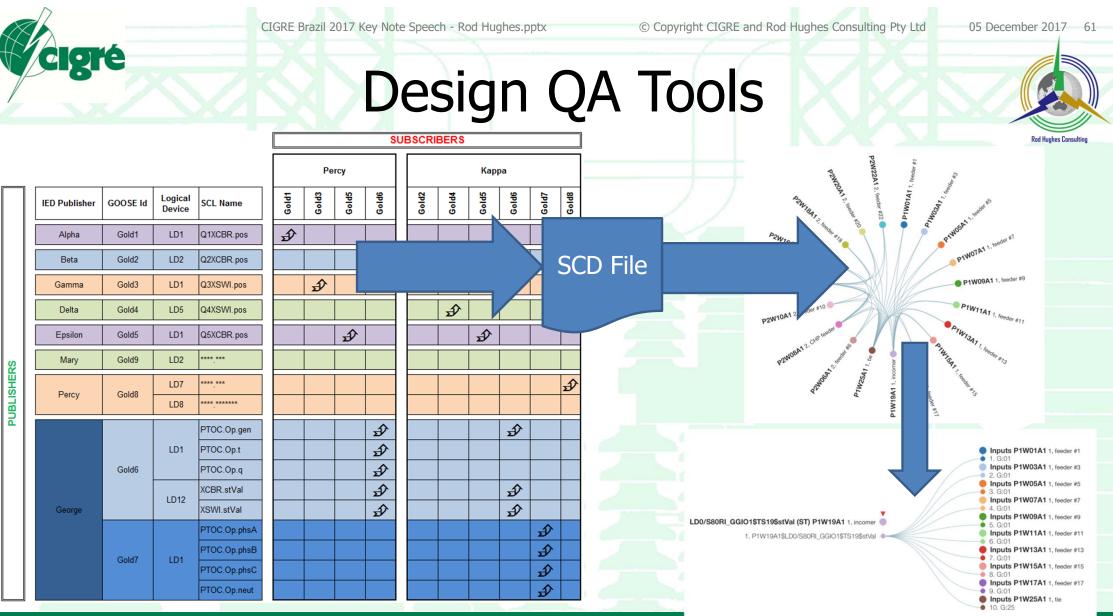
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## The Single Tool?







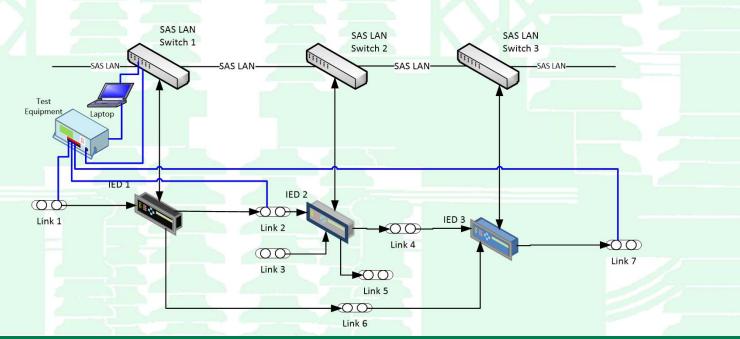
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## Hybrid Test Environment

- Must consider complete system
  - Wire based signals
  - Virtual signals



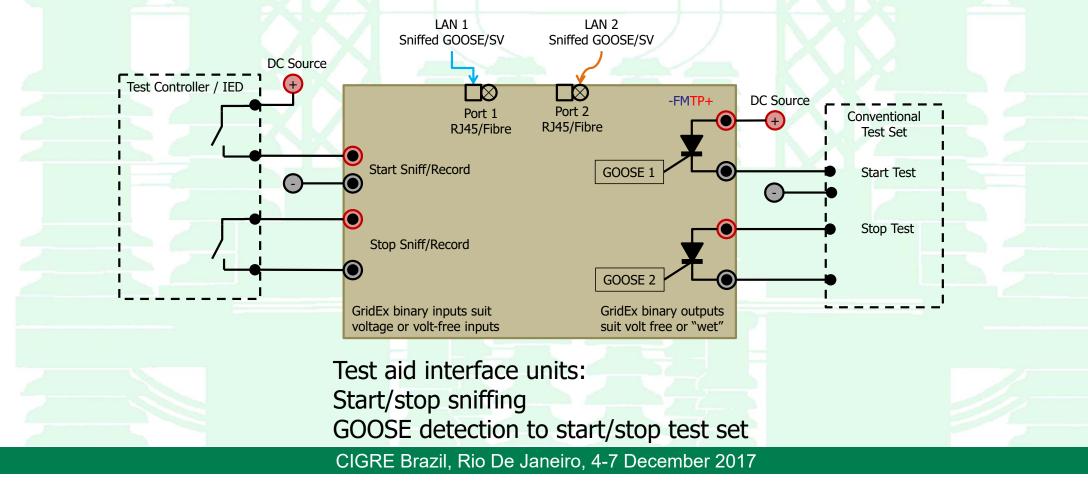


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### Don't throw out old test sets

New IEC 61850 test sets provide extensive IEC 61850 capabilities ... but VERY expensive





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### **Automated Auditing Tools**



Main Compare Ana	lysis View A	B File	Config GridEx®
Compared in A (29)	Value	Compared in B (34)	Value
	01.00:CD:01.00:00 01.00:CD:01.00:08 01.00:CD:01.01:02 01.00:CD:01.01:02 01.00:CD:01.00:00 01.00:CD:01.00:01 01.00:CD:01.00:01 01.00:CD:01.01:08 01.00:CD:01.01:08 01.00:CD:01.01:07 01.00:CD:01.01:07 01.00:CD:01.01:07 01.00:CD:01.01:06 01.00:CD:01.01:06 01.00:CD:01.01:06 01.00:CD:01.01:06 01.00:CD:01.01:06 01.00:CD:01.01:04 01.00:CD:01.01:04		01-0C:CD-01-00:00 \$ 01-0C:CD-01-00:00 \$ 01-0C:CD-01-01-02 \$ 01-0C:CD-01-01-02 \$ 01-0C:CD-01-01-03 \$ 01-0C:CD-01-00:01 \$ 01-0C:CD-01-00:01 \$ 01-0C:CD-01-02-05 \$ 01-0C:CD-01-01-04 \$ 01-0C:CD-01-01-04 \$ 01-0C:CD-01-01-04 \$ 01-0C:CD-01-01-05 \$ 01-0C:
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•	••	<b>▲</b>	

Compared in A (29)         Value         Compared in B (29)         Value           IED3LD0 : OSCILLATOR         01:0C:CD:01:03:01         IED3LD0 : OSCILLATOR         01:0C:CD:01:03:01         IED3LD0 : OSCILLATOR         01:0C:CD:01:03:01           IED2LD0 : gcbTRIP         01:0C:CD:01:02:03         IED3LD0 : SP16GGI0_1TOS         IED3CD0 : SP16GGI0_1TOS         IED3CD0 : SP16GGI0_1TOS         IED3CD0 : SP16GGI0_1TOS         IED3CD0 : SP16GI0_1TOS         IED3CD0 : SP16GI0_1TOS         IED3CD0 : SP16GI0_1TOS         IED3CD0 : SP16GI0_1TOS         IED3CD0 : SP16D10 : IIO2CD0:IIO10:06         IED3LD0 : SP16D10 : IIO2CD0:IIO1:02:05         IED3LD0 : SP16D10 : IIO2CCD0:IIO1:02:05         IED3LD0 : SP16D10 : IIO2CCD0:IIO1:02:05         IED3LD0 : SP16D10 : IIO2CCD0:IIO1:02:05         IED3LD0 : SP16D10 : IIO2CCD0:IIO1:00:06         IED3LD0 : SP16	i <b>dEx</b> ®	Grid	Config	File	В	Α	View	Analysis	Compare	Main
□         IED2LD0 : gcbTRIP         01:0C:CD:01:02:03         □         IED2LD0 : gcbTRIP         01:0C:CD:01:02:03           □         IED3LD0 : SP16GGIO_1TO5         01:0C:CD:01:03:02         □         IED3LD0 : SP16GGIO_1TO5         01:0C:CD:01:03:02           □         IED3LD0 : SP16GGIO_1TO5         01:0C:CD:01:00:00         □         IED3LD0 : SP16GGIO_1TO5         01:0C:CD:01:00:00           □         P139Svstem : qcb02         01:0C:CD:01:00:00         □         P139Svstem : qcb02         01:0C:CD:01:00:03           □         S7SJ64CTRL : Control_Dat         01:0C:CD:01:00:08         □         S7SJ64CTRL : Control_Dat         01:0C:CD:01:00:01           □         S7SJ64CTRL : HA8_Inter         01:0C:CD:01:00:06         □         S7SJ64CTRL : Control_Dat         01:0C:CD:01:00:01           □         IED1LD0 : PHPIOC         01:0C:CD:01:00:06         □         IED2LD0 : PHPIOC         01:0C:CD:01:02:05           □         IED1LD0 : MSQI         01:0C:CD:01:01:06         □         IED1LD0 : PLO         01:0C:CD:01:01:06           □         IED3LD0 : PDIS1         01:0C:CD:01:01:06         □         IED1LD0 : PIS1         01:0C:CD:01:01:06           □         IED1LD0 : PLD         01:0C:CD:01:01:08         □         IED1LD0 : PIS1         01:0C:CD:01:01:06           □	Value		Compared in B (29)			Value	29)	Compared in A (29)		
□       HED3LD0: SP16GGIO_1TOS       01:0C:CD:01:03:02       □       HED3LD0: SP16GGIO_1TOS       01:0C:CD:01:03:02         □       A130BL758CB1: gcbBFS       01:0C:CD:01:00:00       □       A130BL758CB1: gcbBFS       01:0C:CD:01:00:00         □       P139System: qcb02       01:0C:CD:01:00:03       □       A130BL758CB1: gcbBFS       01:0C:CD:01:00:03         □       S7SJ64CTRL: Control_Dat       01:0C:CD:01:00:03       □       S7SJ64CTRL: Control_Dat       01:0C:CD:01:00:06         □       HED2LD0: PHPIOC       01:0C:CD:01:00:02       □       S7SJ64CTRL: HA8_Inter       01:0C:CD:01:00:02         □       HED1LD0: PHPIOC       01:0C:CD:01:01:02:05       □       HED1LD0: CD:01:00:02:05       □         □       HED3LD0: PHPIOC       01:0C:CD:01:01:06       □       □       IED2LD0: PHPIOC       01:0C:CD:01:01:06         □       HED3LD0: PDIS1       01:0C:CD:01:01:06       □       IED1LD0: PLD       01:0C:CD:01:01:06         □       IED3LD0: PDIS1       01:0C:CD:01:01:06       □       IED3LD0: PDIS1       01:0C:CD:01:01:06         □       IED3LD0: PDIS1       01:0C:CD:01:01:08       □       IED1LD0: PLD       01:0C:CD:01:01:04         □       IED1LD0: PSCH       01:0C:CD:01:01:08       □       IED1LD0: PSCH       01:0C:CD:		0:01:03:01	01:0C:CD:0	CILLATOR	IED3LD0 : OS	÷	C:CD:01:03:01	OR 01:00	D0 : OSCILLATO	⊕ IED3LC
⊕       A130BL758CB1 : gcbBFS       01:0C:CD:01:00:00       ⊕       A130BL758CB1 : gcbBFS       01:0C:CD:01:00:00         ⊕       P1395ystem : gcb02       01:0C:CD:01:00:03       ⊕       P1395ystem : gcb02       01:0C:CD:01:00:03         ⊕       S75/64CTRL : Control_Dat       01:0C:CD:01:00:01       ⊕       S75/64CTRL : Control_Dat       01:0C:CD:01:00:08         ⊕       S75/64CTRL : HA8_Inter       01:0C:CD:01:00:02       ⊕       F75/64CTRL : Control_Dat       01:0C:CD:01:00:08         ⊕       S75/64CTRL : HA8_Inter       01:0C:CD:01:00:02       ⊕       IED2LD0 : PHPIOC       01:0C:CD:01:00:06         ⊕       IED1LD0 : MSQI       01:0C:CD:01:01:06       ⊕       IED2LD0 : PHPIOC       01:0C:CD:01:00:06         ⊕       IED1LD0 : PLD       01:0C:CD:01:01:06       ⊕       IED1LD0 : MSQI       01:0C:CD:01:01:06         ⊕       IED1LD0 : PLD       01:0C:CD:01:01:03:05       ⊕       IED1LD0 : PLD       01:0C:CD:01:01:06         ⊕       IED1LD0 : PLD       01:0C:CD:01:01:04       ⊕       IED1LD0 : PLD       01:0C:CD:01:01:04         ⊕       IED1LD0 : PSCH       01:0C:CD:01:01:08       ⊕       IED1LD0 : MXU1       01:0C:CD:01:01:08         ⊕       IED1LD0 : MMXU1       01:0C:CD:01:01:05       ⊕       IED1LD0 : MMXU1       01:0C:CD:0		0:01:02:03	01:0C:CD:0	TRIP	IED2LD0 : gcl	. É	C:CD:01:02:03	01:00	D0 : gcbTRIP	⊕- IED2LC
⊕       P139System : qcb02       01:0C:CD:01:00:03       ⊕       P139System : qcb02       01:0C:CD:01:00:03         ⊕       S7Sj64CTRL : Control_Dat       01:0C:CD:01:00:08       ⊕       S7Sj64CTRL : Control_Dat       01:0C:CD:01:00:08         ⊕       S7Sj64CTRL : Control_Dat       01:0C:CD:01:00:02       ⊕       S7Sj64CTRL : Control_Dat       01:0C:CD:01:00:02         ⊕       IED2LD0 : PHPIOC       01:0C:CD:01:00:06       ⊕       IED2LD0 : PHPIOC       01:0C:CD:01:00:06         ⊕       IED1LD0 : MSQI       01:0C:CD:01:01:06       ⊕       IED2LD0 : PHPIOC       01:0C:CD:01:01:06         ⊕       IED1LD0 : PSQI       01:0C:CD:01:01:06       ⊕       IED2LD0 : PHPIOC       01:0C:CD:01:01:06         ⊕       IED1LD0 : PSQI       01:0C:CD:01:01:06       ⊕       IED2LD0 : PHPIOC       01:0C:CD:01:01:06         ⊕       IED1LD0 : PSQI       01:0C:CD:01:01:06       ⊕       IED2LD0 : PHPIOC       01:0C:CD:01:01:06         ⊕       IED1LD0 : PDS1       01:0C:CD:01:01:06       ⊕       IED1LD0 : PID       01:0C:CD:01:01:04         ⊕       IED1LD0 : PSCH       01:0C:CD:01:01:08       ⊕       IED1LD0 : PSCH       01:0C:CD:01:01:05         ⊕       IED1LD0 : MMXU1       01:0C:CD:01:00:00       ⊕       AAI735kVLIAILD0 : ABB       01:0C:CD:01:00:		0:01:03:02	01:0C:CD:0	6GGIO 1TO5	IED3LD0 : SP	÷	C:CD:01:03:02	1TO5 01:00	D0 : SP16GGIO	⊕- IED3LC
⊕       S75j64CTRL : Control_Dat       01:0C:CD:01:00:08       ⊕       S75j64CTRL : Control_Dat       01:0C:CD:01:00:08         ⊕       S75j64CTRL : HA8_Inter       01:0C:CD:01:00:1C       ⊕       S75j64CTRL : HA8_Inter       01:0C:CD:01:00:1C         ⊕       IED2LD0 : PHPIOC       01:0C:CD:01:00:1C       ⊕       S75j64CTRL : HA8_Inter       01:0C:CD:01:00:1C         ⊕       IED2LD0 : PHPIOC       01:0C:CD:01:02:05       ⊕       IED1LD0 : PHPIOC       01:0C:CD:01:01:06         ⊕       IED3LD0 : PHOIS1       01:0C:CD:01:01:06       ⊕       IED1LD0 : NSQI       01:0C:CD:01:01:06         ⊕       IED1LD0 : PLD       01:0C:CD:01:01:06       ⊕       IED1LD0 : DIS1       01:0C:CD:01:01:06         ⊕       IED1LD0 : PDIS1       01:0C:CD:01:01:06       ⊕       IED1LD0 : DIS1       01:0C:CD:01:01:06         ⊕       IED1LD0 : PSCH       01:0C:CD:01:01:08       ⊕       IED1LD0 : PSCH       01:0C:CD:01:01:08         ⊕       IED1LD0 : MMXU1       01:0C:CD:01:01:05       ⊕       IED1LD0 : MMXU1       01:0C:CD:01:01:05         ⊕       IED1LD0 : MMXU1       01:0C:CD:01:00:00       ⊕       AAI735KVLIALLD0 : ABB       01:0C:CD:01:00:00		0:01:00:00	01:0C:CD:0	1 : gcbBFS	A130BL758C	Ē	C:CD:01:00:00	BFS 01:00	L7S8CB1 : gct	⊕- A130B
⊕       S75j64CTRL: HA8_Inter       01:0C:CD:01:00:1C       ⊕       S75j64CTRL: HA8_Inter       01:0C:CD:01:00:1C         ⊕       IED2LD0: PHPIOC       01:0C:CD:01:02:05       ⊕       IED2LD0: PHPIOC       01:0C:CD:01:02:05         ⊕       IED1LD0: MSQI       01:0C:CD:01:02:05       ⊕       IED2LD0: PMPIOC       01:0C:CD:01:02:05         ⊕       IED1LD0: MSQI       01:0C:CD:01:01:06       ⊕       IED3LD0: PDISI       01:0C:CD:01:03:05         ⊕       IED1LD0: PLD       01:0C:CD:01:01:04       ⊕       IED1LD0: PLD       01:0C:CD:01:01:04         ⊕       IED1LD0: PSCH       01:0C:CD:01:01:08       ⊕       IED1LD0: PSCH       01:0C:CD:01:01:08         ⊕       IED1LD0: MMXUI       01:0C:CD:01:01:00       ⊕       IED1LD0: MXUI       01:0C:CD:01:01:08         ⊕       IED1LD0: MMXUI       01:0C:CD:01:00:00       ⊕       AA1735kVLIA1LD0: ABB       01:0C:CD:01:00:00		0:01:00:03	01:0C:CD:0	gcb02	P139System	÷	C:CD:01:00:03	01:00	vstem : gcb02	⊕ P1395
□         IED2LD0 : PHPIOC         01:0C:CD:01:02:05         □         IED2LD0 : PHPIOC         01:0C:CD:01:02:05           □         IED1LD0 : MSQI         01:0C:CD:01:01:06         □         IED1LD0 : MSQI         01:0C:CD:01:01:06           □         IED1LD0 : PIS1         01:0C:CD:01:01:06         □         IED3LD0 : PDIS1         01:0C:CD:01:01:06           □         IED1LD0 : MSQI         01:0C:CD:01:01:06         □         IED3LD0 : PDIS1         01:0C:CD:01:01:06           □         IED1LD0 : PIS1         01:0C:CD:01:01:03:05         □         IED1LD0 : PIS1         01:0C:CD:01:01:04           □         IED1LD0 : PSCH         01:0C:CD:01:01:08         □         IED1LD0 : PSCH         01:0C:CD:01:01:05           □         IED1LD0 : MMXU1         01:0C:CD:01:00:00         □         IED1LD0 : MMXU1         01:0C:CD:01:00:00           □         AA1735kVL1A1LD0 : ABB         01:0C:CD:01:00:00         □         AA1735kVL1A1LD0 : ABB         01:0C:CD:01:00:00		0:01:00:08	01:0C:CD:0	Control Dat	S75[64CTRL :	Ē	C:CD:01:00:08	Dat 01:00	4CTRL : Contro	⊕- S7SI64
⊕         IED1LD0 : MSQI         01:0C:CD:01:01:06         ⊕         IED1LD0 : MSQI         01:0C:CD:01:01:06           ⊕         IED3LD0 : PDIS1         01:0C:CD:01:03:05         ⊕         IED3LD0 : PDIS1         01:0C:CD:01:03:05           ⊕         IED1LD0 : PLD         01:0C:CD:01:01:04         ⊕         IED1LD0 : PLD         01:0C:CD:01:01:04           ⊕         IED1LD0 : PSCH         01:0C:CD:01:01:05         ⊕         IED1LD0 : PSCH         01:0C:CD:01:01:08           ⊕         IED1LD0 : MXU1         01:0C:CD:01:01:05         ⊕         IED1LD0 : MXU1         01:0C:CD:01:01:05           ⊕         IED1LD0 : MXU1         01:0C:CD:01:00:00         ⊕         AA1735kVL1A1LD0 : ABB         01:0C:CD:01:00:00		0:01:00:1C	01:0C:CD:0	HA8 Inter	S75[64CTRL :	÷	C:CD:01:00:1C	nter 01:00	4CTRL : HA8 In	⊕- S7SI64
⊕-         IED3LD0 : PDIŠ1         01:0C:CD:01:03:05         ⊕-         IED3LD0 : PDIŠ1         01:0C:CD:01:03:05           ⊕-         IED1LD0 : PLD         01:0C:CD:01:01:04         ⊕-         IED1LD0 : PLD         01:0C:CD:01:01:04           ⊕-         IED1LD0 : PSCH         01:0C:CD:01:01:08         ⊕-         IED1LD0 : PSCH         01:0C:CD:01:01:05           ⊕-         IED1LD0 : MMXU1         01:0C:CD:01:01:05         ⊕-         IED1LD0 : MMXU1         01:0C:CD:01:00:05           ⊕-         AA1735kVL1A1LD0 : ABB         01:0C:CD:01:00:00         ⊕-         AA1735kVL1A1LD0 : ABB         01:0C:CD:01:00:00		0:01:02:05	01:0C:CD:0		IED2LD0 : PH	. E	C:CD:01:02:05	01:00	D0 : PHPIOC	B- IED2LC
⊕         IED1LD0: PLD         01:0C:CD:01:01:04         ⊕         IED1LD0: PLD         01:0C:CD:01:01:04           ⊕         IED1LD0: SPCH         01:0C:CD:01:01:08         ⊕         IED1LD0: SPCH         01:0C:CD:01:01:08           ⊕         IED1LD0: MMXU1         01:0C:CD:01:01:08         ⊕         IED1LD0: SPCH         01:0C:CD:01:01:05           ⊕         IED1LD0: MMXU1         01:0C:CD:01:00:00         ⊕         AA1735kVL1A1LD0: ABB         01:0C:CD:01:00:00		0:01:01:06	01:0C:CD:0	QL	IED1LD0 : MS	÷	C:CD:01:01:06	01:00	D0 : MSQI	⊕- IED1L0
⊕-         IED1LD0 : PSCH         01:0C:CD:01:01:08         ⊕-         IED1LD0 : PSCH         01:0C:CD:01:01:08           ⊕-         IED1LD0 : MMXU1         01:0C:CD:01:01:05         ⊕-         IED1LD0 : MMXU1         01:0C:CD:01:01:05           ⊕-         AA1735kVL1A1LD0 : ABB         01:0C:CD:01:00:00         ⊕-         AA1735kVL1A1LD0 : ABB         01:0C:CD:01:00:00		0:01:03:05	01:0C:CD:0	51	IED3LD0 : PD	Ē	C:CD:01:03:05	01:00	D0 : PDIS1	E IED3LC
⊕-         IED1LD0 : MMXU1         01:0C:CD:01:01:05         ⊕-         IED1LD0 : MMXU1         01:0C:CD:01:01:05           ⊕-         AA1735kVL1A1LD0 : ABB         01:0C:CD:01:00:00         ⊕-         AA1735kVL1A1LD0 : ABB         01:0C:CD:01:00:00		0:01:01:04	01:0C:CD:0	)	IED1LD0 : PLI	÷	C:CD:01:01:04	01:00	D0 : PLD	⊕- IED1L0
👾 AA1735kVL1A1LD0 : ABB 01:0C:CD:01:00:00 👘 AA1735kVL1A1LD0 : ABB 01:0C:CD:01:00:00		0:01:01:08	01:0C:CD:0	н	IED1LD0 : PS	Ē	C:CD:01:01:08	01:00	D0 : PSCH	E IED1LC
		0:01:01:05	01:0C:CD:0	XU1	IED1LD0 : MM	÷	C:CD:01:01:05	01:00	D0 : MMXU1	⊕ IED1L0
⊕ AA1735kVL1A1LD0 : ABB 01:0C:CD:01:00:05 ⊡ AA1735kVL1A1LD0 : ABB 01:0C:CD:01:00:05		0:01:00:00	01:0C:CD:0	1LD0 : ABB	AA1735kVL1A	Ē	C:CD:01:00:00	ABB 01:00	5kVL1A1LD0 :	⊕ AA173
		0:01:00:05	01:0C:CD:0	1LD0 : ABB	AA1735kVL14	÷	C:CD:01:00:05	ABB 01:00	5kVL1A1LD0 :	↔ AA173
	••				2					

Validating contractor's "as built" files are the "as operating" files, and vice versa

Verifying nothing has changed from "last year" to now

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## Not just Protection, SCADA

- IEC 61850 90-3 Condition Monitoring
  - Gas Insulated Switchgear
  - Transformer

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- Tap Changer
- Underground Cables
- Transmission Lines
- Auxiliary Power systems

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Controls

1

1

1

1

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7

19

4

4

11

11

6

10

## **Transformer Condition Monitoring**



Settings

22

2

3

1

Wireless Communication Description Logical Measured Status Conservator tank membrane monitor Node Values Supervision Insulation SIML 30 medium (liquid) **Bushing Monitor** SPDC Monitoring and diagnostics 2 for partial discharges Temperature Power Transformer SPTR 6 Monitor Supervision Supervision Insulation 3 SIMA moisture and aging (solid) **OLTC** control and monitoring Bubbling temperature SBTP 3 supervision Gas and Moisture ZBSH Bushing 2 in Oi Cooling group control CCGR 4 Monitor dry contacts 155 data objects and mA signals and many more data attributes 10 - 20 sensors/controllers

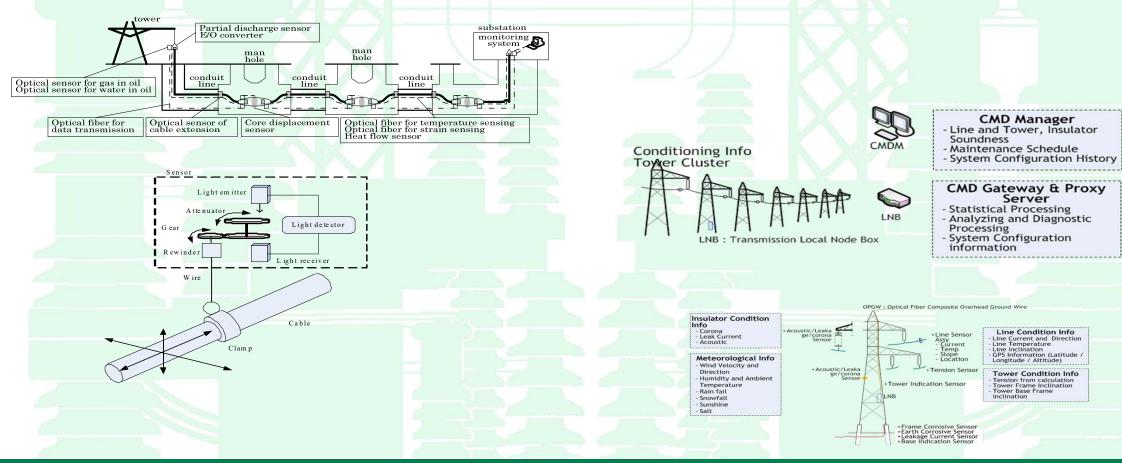
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#### Under Ground Cable, Over Head Line



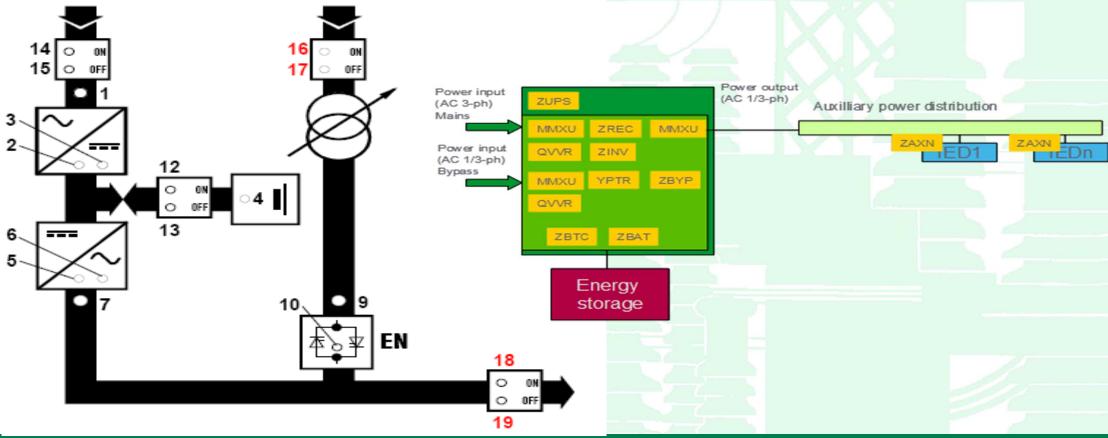


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#### Secured AC system AC input, AC backup, Inverter supply



#### Technical Brochures

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• 246: The Automation Of New And Existing Substations: Why And How, 2004

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- 326: The Introduction Of IEC 61850 And Its Impact On Protection And Automation Within Substations, 2007
- 329: Guidelines For Specification And Evaluation Of Substation Automation Systems, 2007
- 401: Functional Testing Of IEC 61850 Based Systems, 2010
- 404: Acceptable Functional Integration in HV Substations, 2010
- 464: Maintenance Strategies for Digital Substation Automation Systems, 2011
- 466: Engineering Guidelines for IEC61850, 2011
- 540: Applications of IEC 61850 Standard to Protection Schemes, 2013
- 628: Documentation Requirements From Design To Operation To Maintenance For Digital Substation Automation Systems, 2015



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#### Specifically on cyber security and networks

- 317 (2007) "Security For Information Systems And Intranets In Electric Power Systems"
- 419 (2010): Treatment of Information Security
- 427 (2010): The Impact of Implementing Cyber Security Requirements Using IEC 61850
- 507 (2012): Communication Architecture for IP-based Substation Applications
- 603 (2014): Application and Management of Cybersecurity Measures for Protection and Control
- 615 (2015) Security architecture principles for digital systems in Electric Power Utilities
- New Working Group B5.66



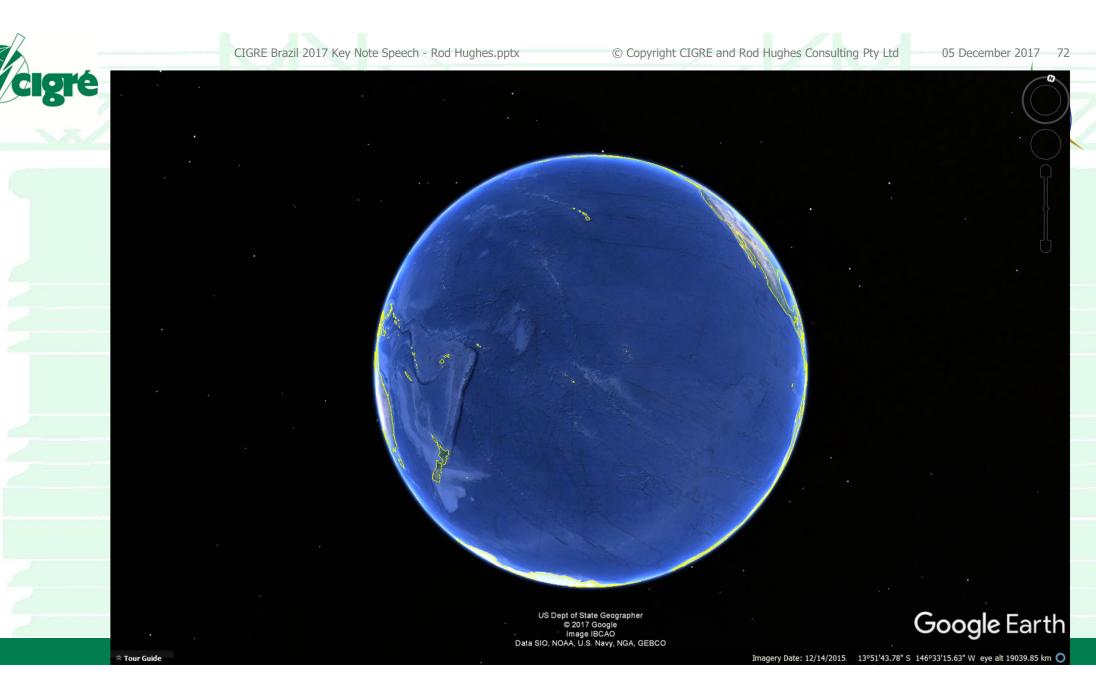
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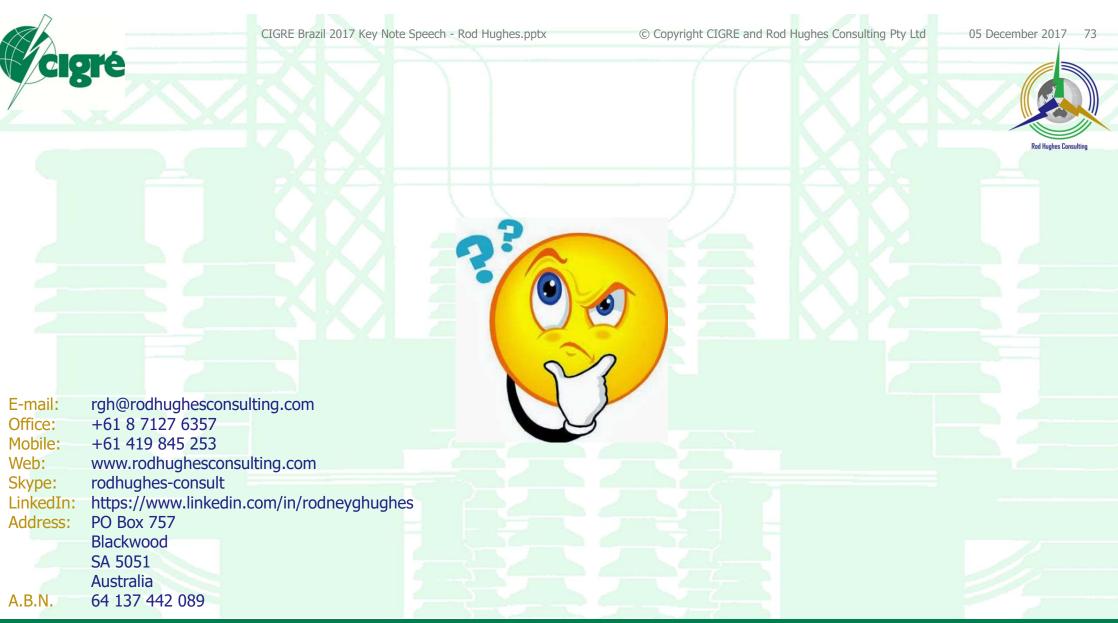
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## Your challenge here...

- What do I NEED to do differently?
- What CAN I do differently?

#### What ELSE do I need to be <u>able</u> to do it differently?





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